

**ABSTRACT:**

In the matrix display driver circuit with an energy recovery inductor (L1), a switch circuit (S3, D3, S6, D9) is connected in parallel with the inductor (L1) to keep the inductor current (IL1) in a loop which is as small as possible, and to keep the voltage (VL1) across the inductor (L1) as low as possible. Consequently, the energy stored in the inductor is lower, and the EMI caused by the parasitic resonance of the inductor (L1) with parasitic capacitances  $C_j$  will be significantly lower.

(Fig. 3)